TRAN	NSMITTAL OI (U	Docket No. BUR920030123US1										
In Re Application Of: Gray et al. FEB 2 0 2004												
	Serial No.	Filing Date TRADEMAR	Examiner		Group Art Unit							
	101,287	12/03/03	Unossignid		Group Art Unit							
Title: CIRCUIT AREA MINIMIZATION USING SCALING												
	Address to: Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450											
		37 C	FR 1.97(b)									
1. 🔀	The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.											
		37 C	FR 1.97(c)									
2.	The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:											
	☐ the stater	ment specified in 37 CFR 1.97(e);	ı									
		OR										
	\Box the fee se	et forth in 37 CFR 1.17(p).										

TRANSMITTAL OF INFORMATION DISCLOS (Under 37 CFR 1.97(b)) 197(c)	Docket No. BUR920030123US1			
In Re Application: Gray et al. FEB 2 0 2004				
Serial No. Filing Date: TRADEMARY 12/03/03 RADEMARY 12/03/03	Examiner- Unossegned	Group Art Unit		
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	ent of Fee s to pay the fee set forth in 37 CFR	1.17(p))		
 □ A check in the amount of is attach ☑ The Director is hereby authorized to charge and cred as described below. □ Charge the amount of ☑ Credit any overpayment. ☑ Charge any additional fee required. Certificate of Transmission by Facsimile* 	dit Deposit Account No. 09-	-0456 (IBM)		
I certify that this document and authorization to charge deposit account is being facsimile transmitted to the United States Patent and Trademark Office (F:	Certificate of Mailing by First Class Mail I certify that this document and fee is being deposited on 02/1/04 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.			
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*This certificate may only be used if paying by deposit account. Signature	Dated: 216/64			

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		"Flexible Image for Pl 1985, pp. 6687-6691.	hysical Layout of	Circuits,"	A. R. Bruss et al., IBM	I Technical Disclos	sure Bulletin, \	Vol. 27, No. 11, April	
		"An Algorithm to Det CH2868-8/90/000-285	ect Positive Cycle 3, 1990, pp. 2853	es in a Cons -2856.	traint Graph for Layo	ut Compaction," I	K. Ishima et al.	, IEEE,	
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Form PTO-A820 (also form PTO-1449)

Docket Number (Optional) Application Number BUR920030123US1 Applicant(s) INFORMATION DISCLOSURE CITATION Gray et al. (Use several sheets if necessary) Filing Date Group Art Unit *EXAMINER OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) "Process Independent Constriant Graph Compaction," D. Boyer, 29th ACM/IEEE Design Automation Conference, Paper 21.4, pp. 318-322. FEB 2 0 2004 "An Algorithm to Compact a VLSI Symbolic Layout with Mixed Constraints," Y. Z. Liao et al., 20th Design Automation Conference, Paper 9.1, IEEE, 1983, pp. 107-112. & TRADE! "Graph-Optimization Techniques for IC Layout and Compaction," G. Kedem et al., 20th Design Automation Conference, Paper 9.2, IEEE, 1983, pp. 113-120. "Symbolic Layout Compaction Review," D. Boyer, 25th ACM/IEEE Design Automation Conference, Paper 26.1, 1988, pp. 383-389. "A Subjective Review of Compaction," Y. Cho, 22nd Design Automation Conference, Paper 25.1, IEEE, 1985, pp. 396-404. "An Introduction to VLSI Physical Design," M. Sarrafzadeh et al., Chapter 7, "Compaction," Section 7.1.2, "Graph-based Techniques," McGraw-Hill (1996), pp. 276-279. "Algorithms for VLSI Physical Design Automation," 3rd Edition, N. Sherwani, Chapter 12, "Compaction," Section 12.3.1, "Constraint-Graph based Compaction," Kluwer Academic Publishers (1999), pp. 453-463.

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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